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Client/Matter/Tkpr:	43889-951-3328	Date:	May 7, 2003 Time Sent:
			Number of pages including this page: 3

TO:

Name:	Examiner Caridad M. Everhart		
Group Art Unit:	2825	Facsimile No.	703 872 9318
Company:	U.S. Patent & Trademark Office	Contact No.	

RE: U.S. Patent Application Serial No. 09/610,640
For: APPARATUS AND METHOD FOR OPTICAL ...
SEMICONDUCTOR DEVICE
Attorney Docket No.: 43889-951

Applicants respectfully request that the attached page No. 30 be substituted for page 30 of the Amendment Under 37 CFR 1.111 filed with the USPTO on May 5, 2003. A copy of the date-stamped postcard is also attached.

Applicants have underlined the new matter which was presented in Claim 56 which underlining was inadvertently omitted in the Amendment filed on May 5, 2003.

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WDC99 754132-1.043889.0951

Serial No.: 09/610,640

calculating a change rate of a reflectance of the measurement light by dividing a difference between the respective reflectances of the measurement light in the presence and absence of said exciting light supplied to said semiconductor region by the reflectance of the measurement light in the absence of the exciting light;

wherein said exciting light is intermittently emitted at a frequency of 1 kHz or less in said step of supplying the exciting light.

56. (Amended) A method of manufacturing a semiconductor device [according to claim 49], said method comprising:

a first step of forming a substrate having a semiconductor region;

a second step of evaluating an optical property of said semiconductor region;

a third step of performing an etching process with respect to said semiconductor region;

and

a fourth step of controlling a condition for said etching process based on an optical property of said semiconductor region evaluated in said second step,

wherein dry etching [using] utilizing a plasma is performed in said third step.

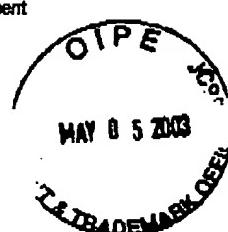
62. (Twice Amended) A method of manufacturing a semiconductor device [according to claim 50], said method comprising:

a first step of forming a substrate having a semiconductor region;

a second step of evaluating an optical property of said semiconductor region;

a third step of performing an etching process with respect to said semiconductor region;

and

Applicant Koji ERIGUCHI, et al.Docket No. 43889-951Title: APPARATUS AND METHOD FOR OPTICAL EVALUATION, APPARATUS AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR DEVICEDate Sent 5/5/2003 Hand Carried Fax Electronic Cert. of Mailing Express Mail No. 09/610,640 Transmittal LetterNew Patent App Utility Design Cont. CIP Div. PCT CPA RCE Prov Other: _____ Letter submitting _____ pages of drawings Req. for Approval of Drawing Amendments Req. for Oral Hearing Not. of Appeal Appeal Brief Reply Brief Rule 312 Amendment/Letter Req. for Acknowledgement of Cited Art Issue Fee Publication Fee Req. for Certificate of Correction Maintenance Fee for _____ years after grant Fee Address Indication Form Terminal Disclaimer Petition to Commissioner Status Inquiry Other Translations 8-296592; 8-350612; 9-015382; 9-189841 Small Entity Large Entity Declaration/Power of Attorney Recordation of Assignment/Security Agreement Information Disclosure StatementForm PTO 1449 copies of cited references Preliminary Amendment Response to Missing Parts Notice Resp. to Notice to Correct App. Papers Certified Copy of Priority Doc. Claim for Convention Priority Response/Amendment to Office Action of Request for month Extension of Time

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THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.

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